



PATENT

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Alexei Sadovnikov

Application No.: 10/669,045

Filed: September 23, 2003

For: LOW DOPED BASED SPACER FOR  
REDUCTION OF Emitter-BASE  
CAPACITANCE IN BIPOLAR  
TRANSISTOR WITH SELECTIVELY  
GROWN EPITAXIAL BASE

Confirmation No.: 3995

Group Art Unit: 2815

Examiner: Jesse A. Fenty

**SUBMISSION OF FORMAL DRAWINGS**

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**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope, addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on February 28, 2005.

STALLMAN & POLLOCK LLP

Dated: 02/28/05 By: Lana T. Brenner  
Lana T. Brenner

Commissioner for Patents  
Attn: Official Draftsperson  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicant encloses three (3) sheet of formal drawing corresponding identically to the informal drawing filed with the above-identified U.S. patent application. Applicant requests that the formal drawing be substituted for the original-filed informal drawing.

Respectfully submitted,

STALLMAN & POLLOCK LLP

Dated: February 28, 2005

By: Michael J. Pollock  
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Atty Docket No.: NSC1-H2610